

The 12th ACM International Symposium on Mobile Ad Hoc Networking and Computing

To be held from May 16-20, 2011 in *Paris, France*

Sponsored by ACM SIGMOBILE

ACM MobiHoc is the premier international symposium dedicated to addressing challenges emerging from wireless ad hoc networking and computing. With its highly selective technical program, the symposium will bring together researchers and practitioners from a broad spectrum of wireless networking research to present the most up-to-date results and achievements in the field.

We invite paper submissions on mobile ad hoc networks, wireless sensor networks, wireless mesh networks, vehicular networks and ad hoc computing systems, with the focus being on issues at and above the MAC layer. It is expected that the symposium program will constitute research papers addressing theoretical challenges and/or practical issues in the wireless network design.

Scope

Areas of interest include, but are not limited to the following:

- Applications and middleware support
- Transport, network, and MAC protocols
- Energy efficiency
- Location discovery
- Cross-layer design and control
- Network resilience, fault-tolerance & reliability
- Functional computation and data aggregation
- Modeling and performance analysis
- Scaling laws and fundamental limits
- Network coding
- Opportunistic and delay-tolerant networks
- Vehicular networks
- Cognitive radio networks
- Distributed sensing, actuation, control, and coordination
- Trust, security and privacy
- System design and testbeds
- Measurements from experimental systems

The symposium especially encourages the submission of exploratory studies that identify new challenges in the network design, innovative services, and applications that may stimulate far-reaching future research. This year's symposium will assign a Best Paper Award among all the papers submitted to the conference.

Important Dates

Paper Abstract Registration: **November 5th 2010** – 9pm Eastern Time

Paper Submission Deadline: **November 12th, 2010** – 9pm Eastern Time

Organizers

General Chair: Philippe Jaquet, INRIA

TPC Co-Chairs: Konstantina (Dina) Papagiannaki, Intel Labs and Devavrat Shah, MIT